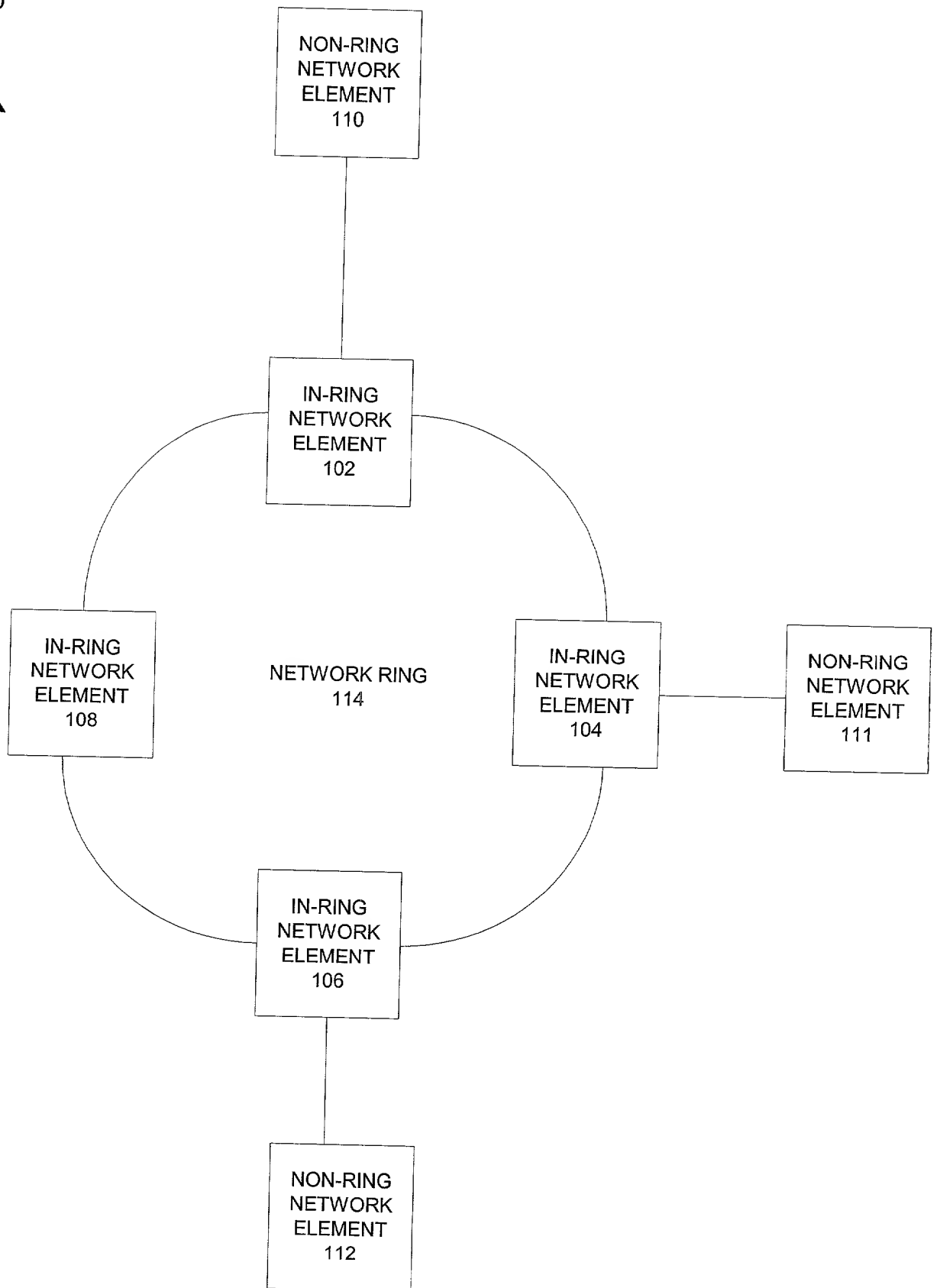


SYSTEM 100



**FIG. 1**

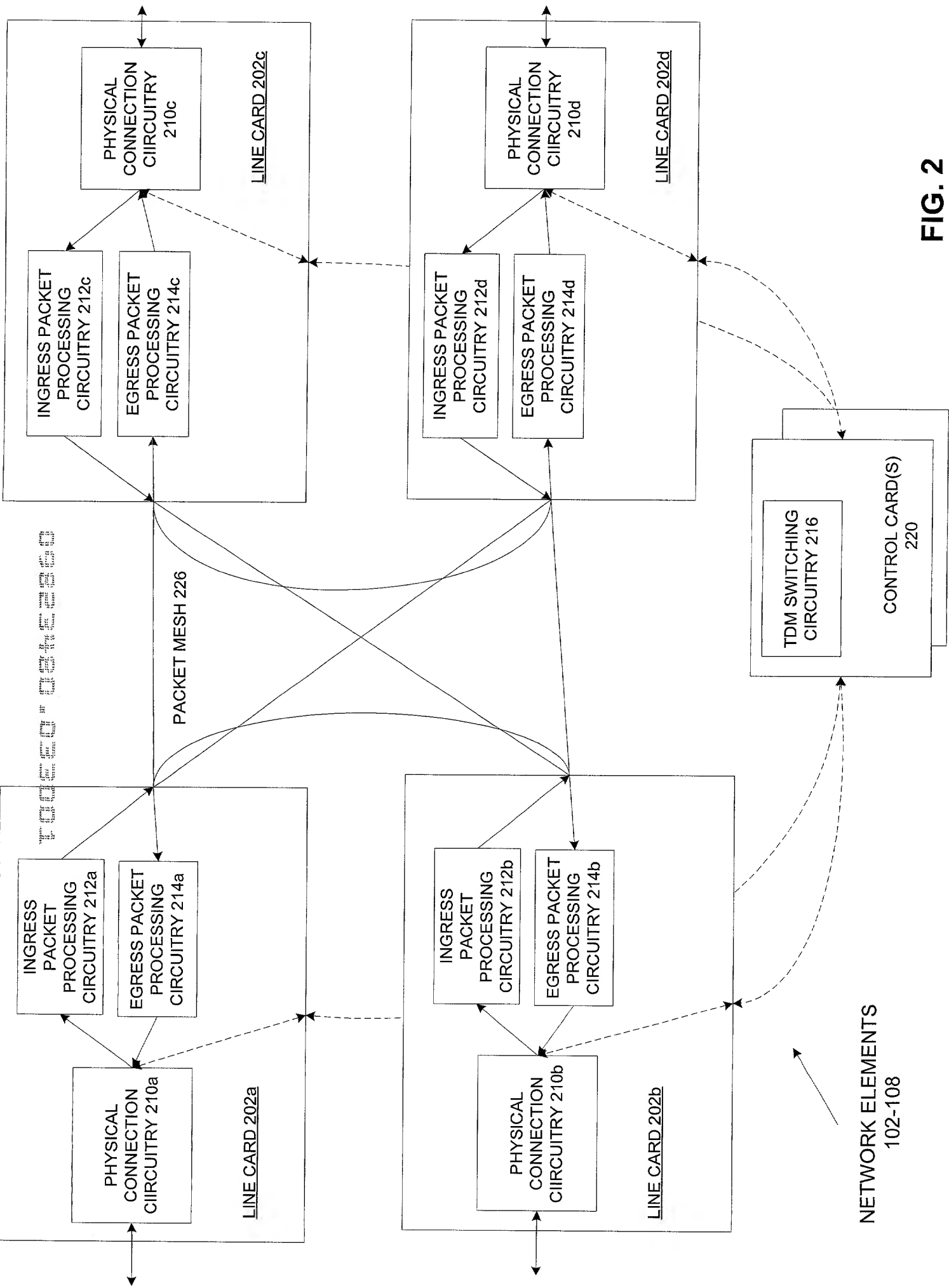


FIG. 2

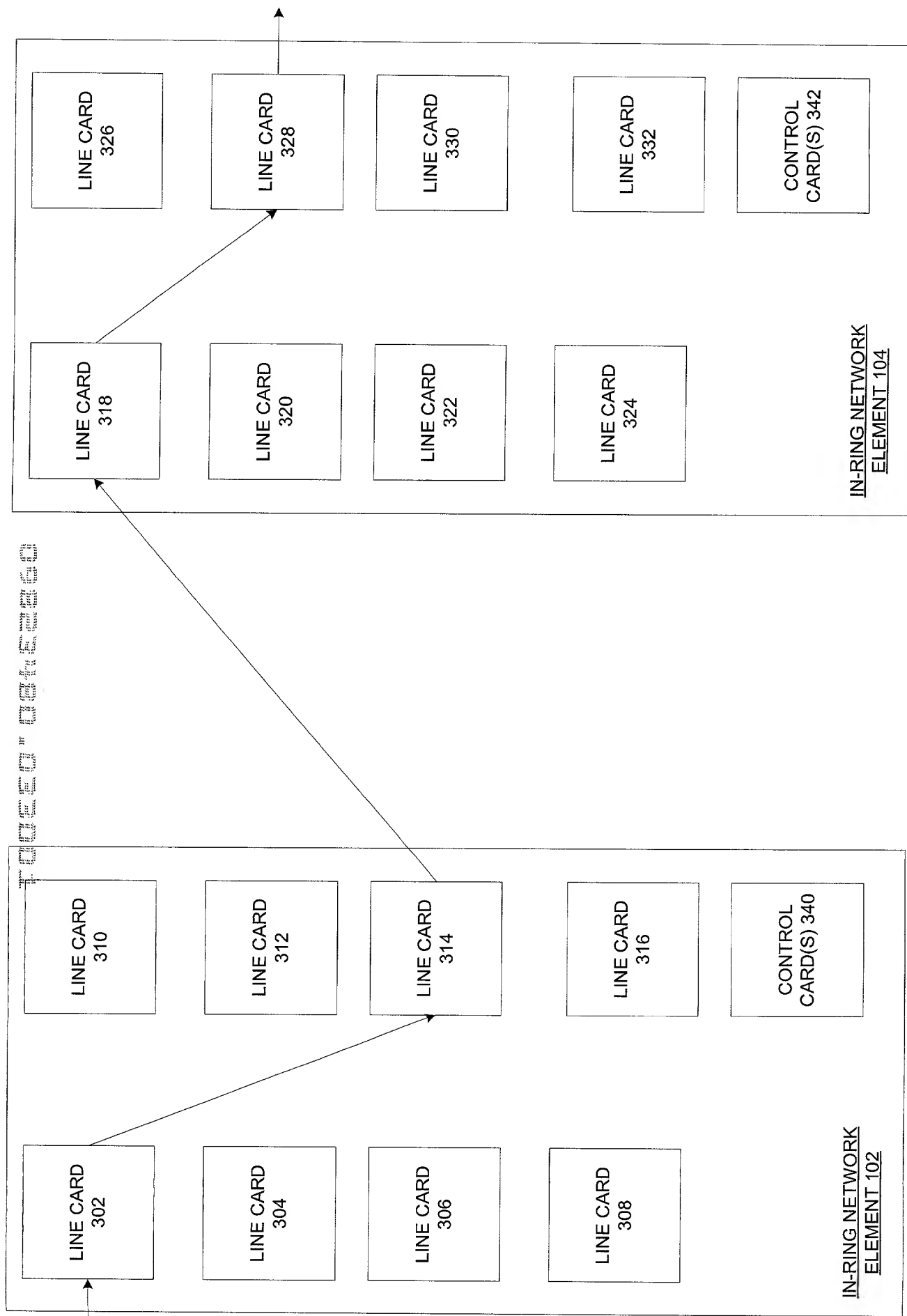


FIG. 3

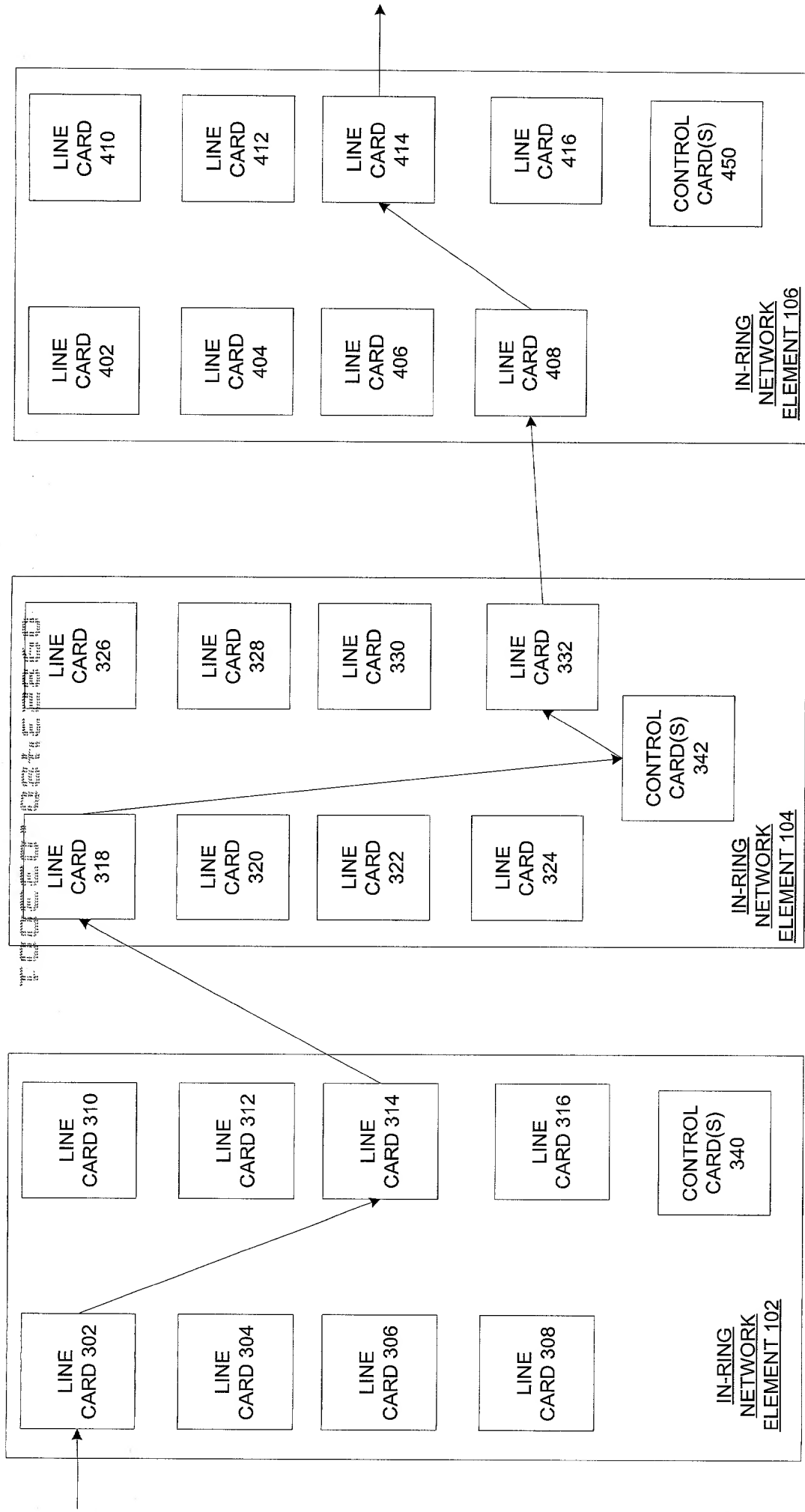


FIG. 4

LINE CARD 202

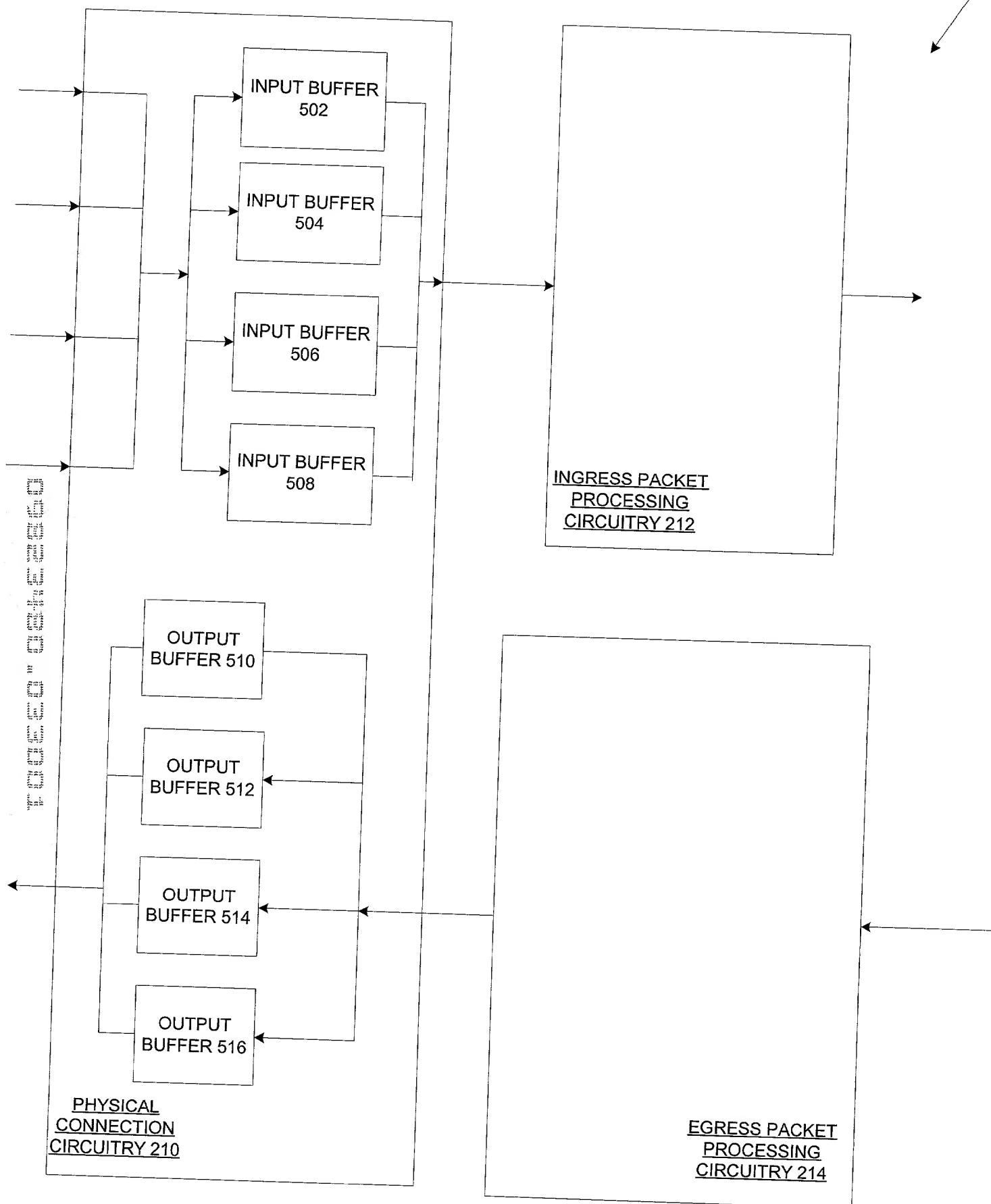
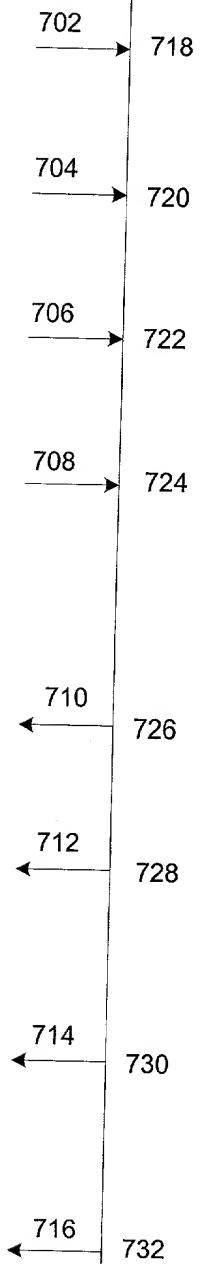


FIG. 5

STS1-9 TDM	STS10-11 PACKETS	STS12-21 TDM	STS22-23 PACKETS	STS24-35 TDM	STS36-37 PACKETS	STS38-44 TDM	STS45-46 PACKETS	STS47-48 TDM	<div>602</div> <div>604</div> <div>606</div> <div>608</div> <div>610</div> <div>612</div> <div>614</div> <div>616</div> <div>618</div>
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**FIG. 6**

FIG. 7 is a block diagram of a physical connection circuitry 210. The circuitry 210 includes a plurality of input/output ports 702, 704, 706, 708, 710, 712, 714, and 716. Each port is connected to a corresponding internal component 718, 720, 722, 724, 726, 728, 730, and 732. The components 718, 720, 722, and 724 are connected to the ports 702, 704, 706, and 708, respectively, via arrows pointing from the ports to the components. The components 726, 728, 730, and 732 are connected to the ports 710, 712, 714, and 716, respectively, via arrows pointing from the components to the ports.



PHYSICAL  
CONNECTION  
CIRCUITRY 210

FIG. 7

METHOD 800

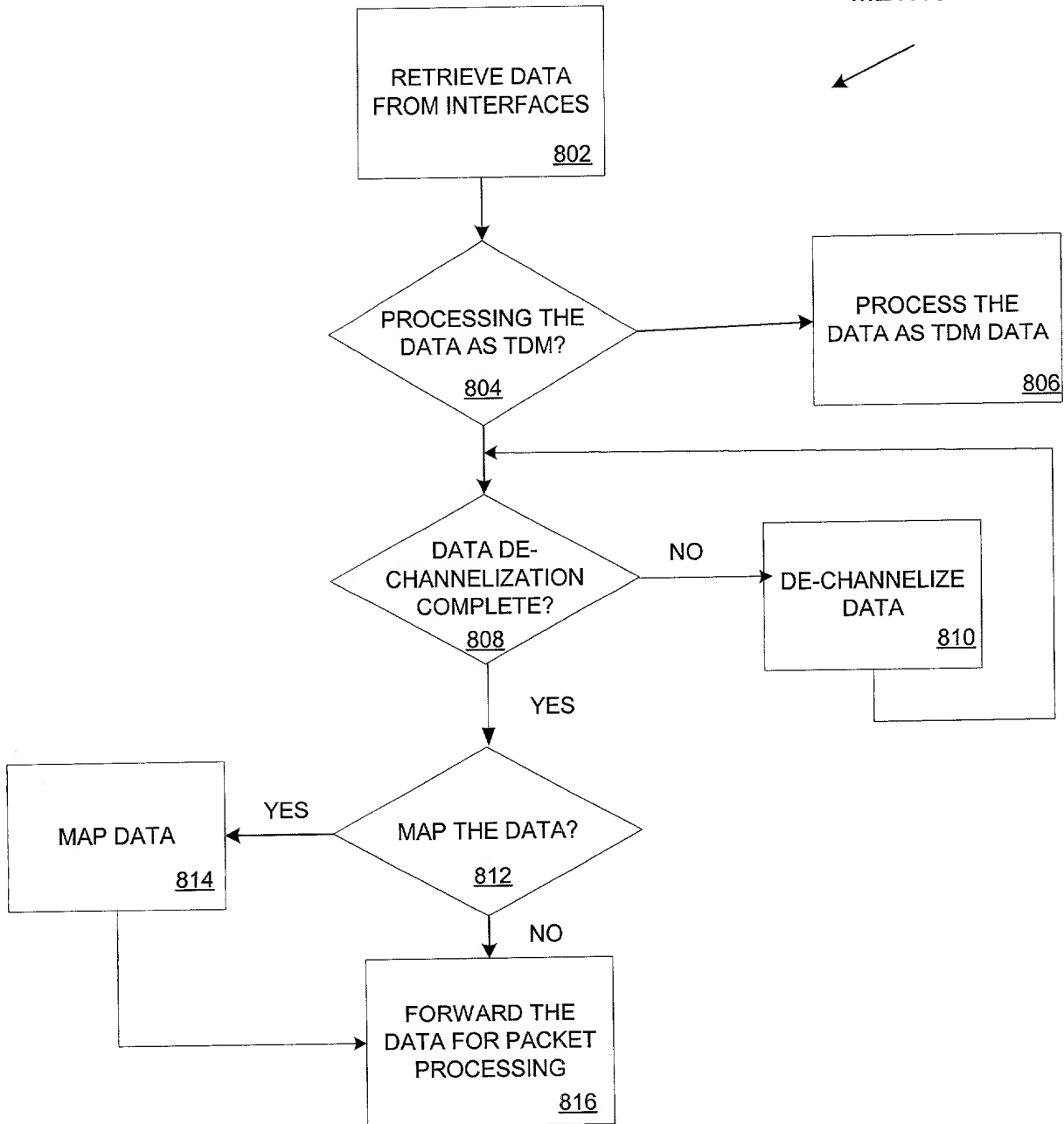


FIG. 8

METHOD 900

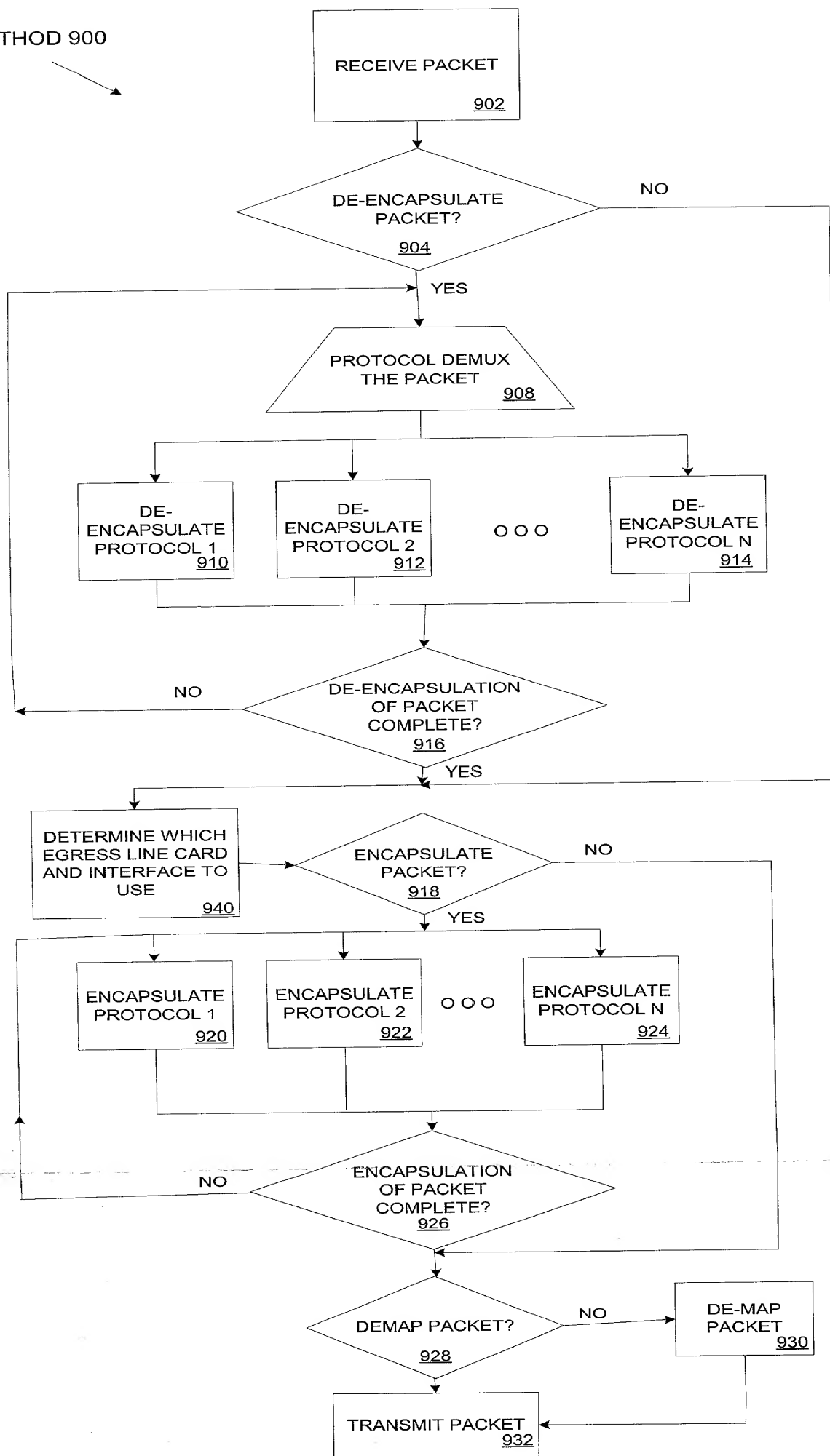
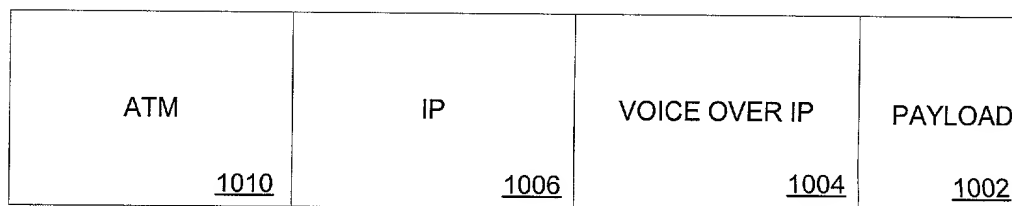
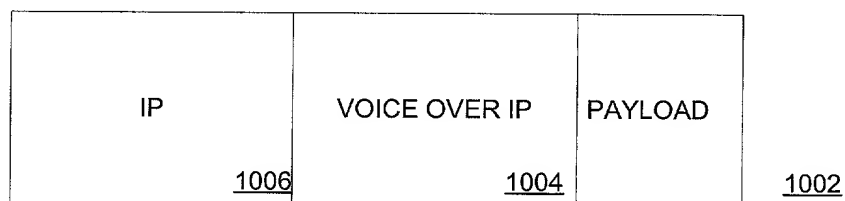


FIG. 9



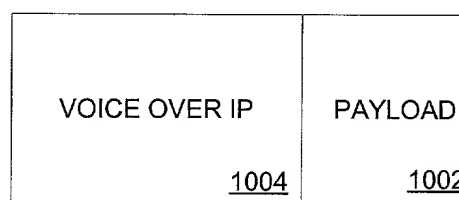
**FIG. 10a**

PACKET 1000



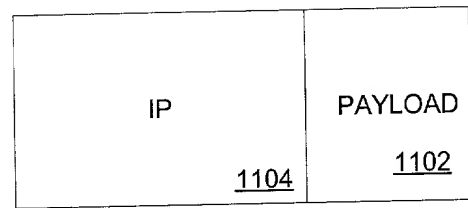
**FIG. 10b**

PACKET 1000



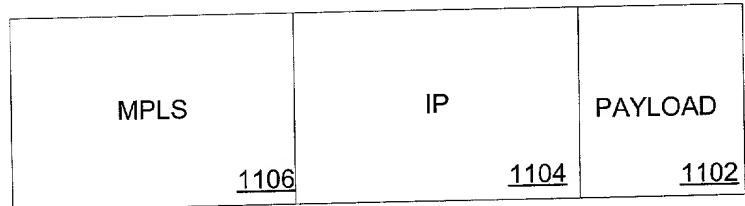
**FIG. 10c**

PACKET 1000



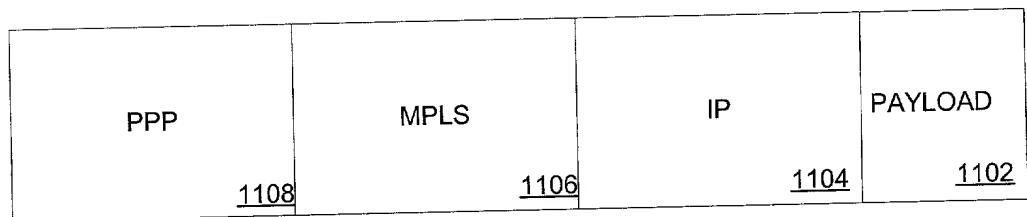
**FIG. 11a**

PACKET 1100



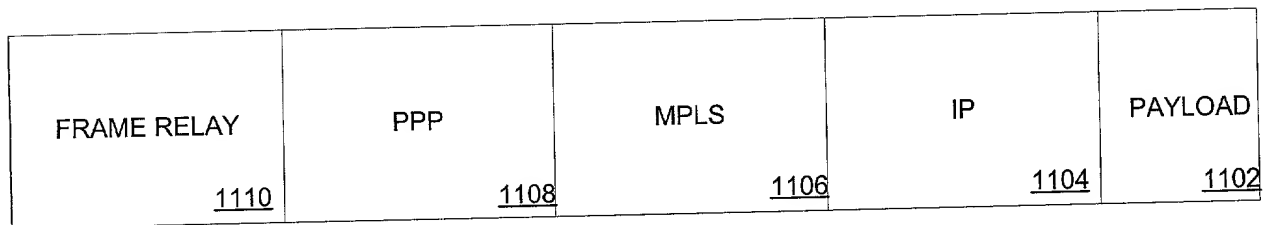
**FIG. 11b**

PACKET 1100



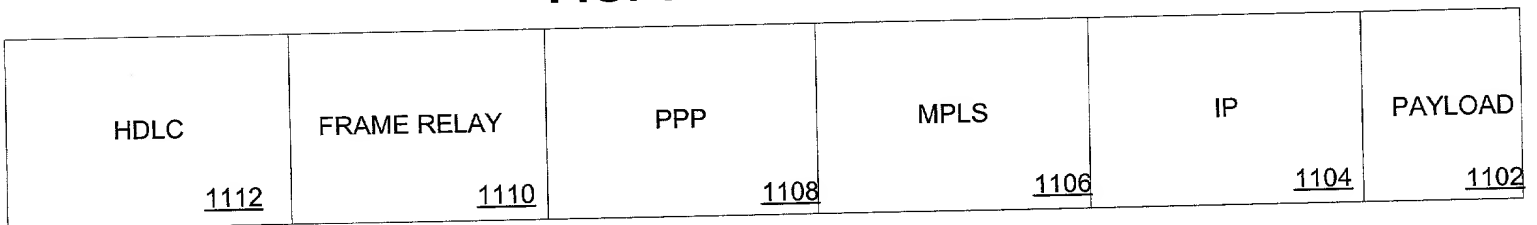
**FIG. 11c**

PACKET 1100



**FIG. 11d**

PACKET 1100



**FIG. 11e**

PACKET 1100